

a1 the n-channel and p-channel transistors and the current driven element during a reproduction stage.

a2 8. (Amended) A driver circuit as claimed in claim 5, further comprising respective further switching means respectively connected to bias the n-channel transistor and the p-channel transistor to act as diodes during the programming stage.

a3 10. (Amended) A driver circuit as claimed in claim 5, wherein the circuit is implemented with polysilicon thin film transistors.

a4 12. (Amended) A driver circuit as claimed in claim 1, wherein the current driven element is an electroluminescent element.

16. (Amended) A method as claimed in claim 13 comprising providing respective storage capacitors for the n-channel and p-channel transistors and respective switching means connected so as to establish when operative respective paths to the n-channel and p-channel transistors for respective data voltage pulses thereby to establish, when operative, a voltage driver circuit for the current driven element.

a5 17. (Amended) A method as claimed in claim 13 comprising providing a programming stage during which the n-channel and p-channel transistors are operated in a first mode and wherein a current path from a source of current data signals is established through the n-channel and the p-channel transistors and the current driven element and wherein a respective operating voltage of the n-channel transistor and the p-channel transistor is stored in respective storage capacitors, and a reproduction stage wherein a second mode and a second current path is established through the n-channel transistor and the p-channel transistor and the current driven element.

a6 19. (Amended) A method of controlling the supply current to an electroluminescent display comprising the method as claimed in claim 13 wherein the current driven element is an electroluminescent element.